# Université du Québec École de technologie supérieure **ODELING AND CONTROL OF THREE-PHASE RECTIFIERS, OPERATING WITH HIGH EFFICIENCY AND LOW HARMONIC DISTORSION: APPLICATION TO THE VIENNA** RECTIFIER

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## OUTLINE

- 1. AC/DC CONVERSION : HISTORY AND APPLICATIONS
- 2. POWER QUALITY: HARMONICS PROBLEMS
- 3. THREE-PHASE POWER FACTOR CORRECTION SWITCHED MODE RECTIFIERS
- 4. INTRODICTION OF THE VIENNA TOPOLOGY
- 5. MAIN SCOPES OF RESEARCH
  - GENERAL METHODOLOGY OF DESIGN
  - MODELING AND EXPERIMENTAL MODEL VERIFICATION
  - POWER CONTROL OF THE RECTIFIER
- 6. SYNTHESIS OF THE PROPOSED CONTROL TECHNIQUES
- 7. SENSORLESS CONTROL USING A NONLINEAR OBSERVER
- 8. CONCLUSION AND CONTRIBUTIONS

#### INTRODUCTION

- 60% of the electric energy, produced in Canada and the USA, transites by static power converters [Hess, 1998].
- AC/DC converters ensure the interface and the adaptation of the energy between the electric grid and the CC loads.

## AC/DC CONVERTERS: HISTORY (1)

- Until the XX<sup>th</sup> century: use of electromechanical rectifier, particularly for the railway electrification systems and the variable speed DC motor drives.
- Those rectifiers are characterized by a low efficiency and a high usure rate.



Nottingham Express Transit

## AC/DC CONVERTERS: HISTORY (2)

- During the 70s: use of thyristors and diodes as input stage in the DC/DC and DC/AC converters.
- During the next years, developpement of power bipolar transistors, thus favorizing the apparition of low and medium power conversion electronics.
- At the earliest 80s: the apparition of transistor-based devices limits the use of the thyristors to the very high power applications.

### AC/DC CONVERTERS: HISTORY (3)

- Since 1985: The use of IGBTs in medium power knows a large extension during 10 ans.
- In 1997: The apparition of IGCTs for voltages higher than 6 kV, risks, at long term, to put an end to the GTO thyristors.
- In 2002: apparition of silicium carbure based components (SiC).
- Since 2004: studies about diamond based components, allowing operation with much higher blocking and thermal abilities.

#### AC/DC CONVERTERS: APPLICATIONS

















#### POWER QUALITY: HARMONICS PROBLEMS













#### ELECTRIC WAVEFORM DISTORSION



### HARMONICS EFFECTS (1)

Phase a Phase b Phase c





NEUTRAL OVERLOAD

**Effects on the transformers** 



triple N harmonics

**Eddy currents** 

## Harmonics effects (2)





Réseau

Installation

#### **Differential current breakers**



#### Voltage distorsion at the PCC

#### **Constraints on the PFCC**





#### Direct:

- > Equipments failure
- Loss of production
- Loss of salaries during improductive periods
- Restarting costs

#### Indirect:

- > Production delays
- Loss of contracts

## COSTS (2)

- The failure of two transformers in a glass factory in Europe may cause a loss of about 600 000 € and of 3 days of production [De Keulenaer, 2003].
- A fire, generated by the overheat of the neutral, may cost until 1 million € [De Keulenaer, 2003].
- The loss of power supply in a telecom edifice may cost the amount of 30 000 € [De Keulenaer, 2003].



# Harmonic Emission Limits in QUEBEC (Inspired from the IEC-61000-4-7)



n impairs						n pairs							
n	3	5	7	- 9	11,	[17,23]	[23,35[	[35,∞[	2	4	6	8	≥10
Scc/Sr					13								
< 20	1	1.2	.8	.5	.5	.4	.3	.2	.75	.5	.3	.2	.15
≥20	1.5	2	1.5	.75	1	.65	.45	.3	1.1	.75	.45	.3	.25
et <50													
≥50	2	3	2	1	1.5	1	.7	.5	1.5	1	.6	.4	.3
et < 200													
$\geq 200$	3	4	3	1.25	2	1.5	1	.7	2.2	1.5	1	.6	.4

$S_{cc}/S_{r}$	TDDc
< 20	1.7
≥20 et <50	3
≥50 et < 200	4.5
≥ <b>200</b>	6

Tableau 1: Limites d'émission des harmoniques (In/Ir %)

#### THREE-PHASE PFC/SMR: CLASSIFICATION

- Commutation type: spontaneous / forced,
- Line currents control: active/ passive / hybrid,
- Isolation (or not) of the output stage,
- Power flow: unidirectionnal / bidirectionnal,
- Conduction mode: continuous / discontinuous

### THREE-PHASE PFC/SMR: ChARACTERISTICS

- Sinusoidal current absorption,
- Resistive characteristics of the mains fundamental,
- Possibility of regulation on the DC side,
- High power density,

#### THREE-PHASE PFC/SMR: TOPOLOGIES



#### INTRODUCTION OF THE VIENNA TOPOLOGY



### CARACTERISTICS



#### **APPLICATIONS**



**Battery chargers** 



UPS



Welding units



Air conditioning



Integrated motors





Aeronautics and maritime

#### VARIANTES











- To propose a general design methodology for the Vienna converter.
- To construct a 1.5 kVA prototype for the experimental validations.



#### SPECIFICATIONS

NOMINAL POWER	1.5 kVA
POWER FACTOR	> 0.97
THREE-PHASE SUPPLY	110 V RMS
TOTAL DC BUS VOLTAGE	500 V
MAXIMUM CURRENT RIPPLE	15%
MAXIMUM VOLTAGE RIPPLE	± 5%
MAXIMUM CURRENT	10 A peak
THD	< 7%
SWITCHING FREQUENCY	constant

# Prevalent switching sequences



## Line currents controllability





#### **RMS/Average currents**



## Power stage specifications

Components	Specifications
Boost inductors	<i>L</i> : (20 mH, 10A), $r_L$ = 1.68 Ω
Filtering capacitors	$C_{dc}$ : (470µF, 450V), $L_c$ = 1.93mH, $r_c$ = 183 m $\Omega$
Decoupling capacitors	2.2 μF. 400V
(dv/dt) snubbers	<i>R</i> <sub>sv</sub> : (50 kΩ, 1W), <i>C</i> <sub>sv</sub> : (2.2 nF, 600V), <i>D</i> <sub>sv</sub> : (15 A, 1200 V)
(di/dt) snubbers	<i>R<sub>si</sub></i> : (1 Ω, 1W), <i>L<sub>si</sub></i> : 1.3 μH, <i>D<sub>si</sub></i> : (15 A, 1200 V)
Four-quadrant switches	$I_{c25} = 50A, V_{CES} = 1200V, V_{CE(sat)typ.} = 2 V$
Rectification diodes	$V_R = 1200 \text{ V}, I_{F,avg} = 15 \text{ A}$

### **POWER CIRCUIT**



#### WAVEFORMS AT NOMINAL POWER



### EXPERIMENTAL WAVEFORMS



### COMPUTED ENTITIES VS MEASURED ENTITIES

	COMPUTED VALUES	MEASURED VALUES
<i>I</i> <sub>DF,avg</sub> (A)	1.07	0.84
<i>I<sub>DF,rms</sub></i> (A)	2.4	2.02
<i>I</i> <sub>DT,avg</sub> (A)	0.94	
<i>I<sub>DT,rms</sub></i> (A)	2.06	
<i>I<sub>T,avg</sub></i> (A)	0.8	0.75
<i>I<sub>T,rms</sub></i> (A)	4	3.8
LOSSES (W)	34.5	51
EFFICIENCY (%)	97.7	96.6

#### **AUXILIARY CIRCUITS**



**Transformers 120V/25V** 





Gate drives

Sensors



**Opto-insulation board** 



**Protection circuit**
#### EXPERIMENTAL SETUP





# MODELING

- To propose reliable models for the Vienna topology, for dynamic analysis and control design aims.
- To experimentally validate the proposed, in order to conclude about their precision level.





# AC SIDE STATE EQUATIONS

#### Configuration: (0,1,0); (*i*<sub>*a*</sub>>0, *i*<sub>*b*</sub>>0, *i*<sub>*c*</sub><0)



$$v_{x} = L \frac{d(i_{x})}{dt} + r_{L}i_{x} + \left[v_{dc}^{+} \frac{\Theta(i_{x})}{0} - v_{dc}^{-} \overline{\Theta(i_{x})}\right] (1 - d_{k}) + v_{M,n}, \quad x = \{a, b, c\}, \quad k = \{1, 2, 3\}$$

$$d_{k}^{+} = 2 \left(1 - d_{k}\right) \left[\frac{v_{dc}^{+} \Theta(i_{x}) - v_{dc}^{-} \overline{\Theta(i_{x})}}{v_{dc}^{+} + v_{dc}^{-}}\right], \quad x \in \{a, b, c\}, \quad k = \{1, 2, 3\}$$

$$v_{x} = L \frac{d(i_{x})}{dt} + r_{L}i_{x} + \frac{v_{dc}}{6} Md_{k}^{+}, \quad M = \begin{pmatrix} 2 & -1 & -1 \\ -1 & 2 & -1 \\ -1 & -1 & 2 \end{pmatrix}$$

#### DC SIDE STATE EQUATIONS



## abc/dqo TRANSFORM

$$\begin{bmatrix} \frac{\mathrm{d}(Ki_{x})}{\mathrm{dt}} \\ C_{dc} \frac{\mathrm{d}(v_{co})}{\mathrm{dt}} \\ C_{dc} \frac{\mathrm{d}(v_{co})}{\mathrm{dt}} \\ C_{dc} \frac{\mathrm{d}(\Delta v_{co})}{\mathrm{dt}} \end{bmatrix} = \begin{bmatrix} \mathbf{k} \cdot \mathbf{k}_{x} + \frac{1}{L} \left( Kv_{x} - r_{L}Ki_{x} - \frac{v_{dc}}{6} MKd_{k}^{'} \right) \\ \frac{3}{2} \left( Kd_{k}^{'} \right)^{T} \left( Ki_{x} \right) - \frac{\Delta v_{dc}}{v_{dc}} \left( Kd_{k}^{'} \right)^{T} \left[ K \operatorname{SGN}^{-1}K^{T} \right]^{-1} \left( Ki_{x} \right) - i_{dc}^{+} - i_{dc}^{-} \\ \left( Kd_{k}^{'} \right)^{T} \left[ K \operatorname{SGN}^{-1}K^{T} \right]^{-1} \left( Ki_{x} \right) - \frac{3}{2} \frac{\Delta v_{dc}}{v_{dc}} \left( Kd_{k}^{'} \right)^{T} \left( Ki_{x} \right) - i_{dc}^{+} + i_{dc}^{-} \end{bmatrix}$$

 $x = \{a, b, c\}, k = \{1, 2, 3\}$ 

# AVERAGE STATE MODEL

$$\begin{aligned} \frac{\mathrm{d}(i_{d})}{\mathrm{dt}} &= \frac{1}{L} \left( v_{d} - r_{L}i_{d} + L\omega_{o}i_{q} - \frac{v_{dc}}{2}d_{d}^{'} \right) \\ \frac{\mathrm{d}(i_{q})}{\mathrm{dt}} &= \frac{1}{L} \left( v_{q} - r_{L}i_{q} - L\omega_{o}i_{d} - \frac{v_{dc}}{2}d_{q}^{'} \right) \\ \frac{\mathrm{d}(v_{co})}{\mathrm{dt}} &= \frac{1}{C_{dc}} \left[ \frac{3}{2} \left( d_{d}^{'}i_{d} + d_{q}^{'}i_{q} \right) - \alpha \frac{\Delta v_{dc}}{v_{dc}} d_{o}^{'}i_{d} - i_{dc}^{+} - i_{dc}^{-} \right] \\ \frac{\mathrm{d}(\Delta v_{co})}{\mathrm{dt}} &= \frac{1}{C_{dc}} \left[ -\frac{3}{2} \frac{\Delta v_{dc}}{v_{dc}} \left( d_{d}^{'}i_{d} + d_{q}^{'}i_{q} \right) + \alpha d_{o}^{'}i_{d} - i_{dc}^{+} + i_{dc}^{-} \right] \end{aligned}$$

### EXPERIMENTAL VALIDATION OF THE LARGE SIGNAL MODEL (LSM)



### VALIDATION 1: PHASE PLANE METHOD



### TRAJECTORIES IN THE PHASE PLANE















#### SMALL SIGNAL MODEL



# TRANSFERT FUNCTIONS DERIVATION

$$\tilde{X}(s) = (sI - A)^{-1} \tilde{B} d(s) + (sI - A)^{-1} \tilde{E} v(s)$$

$$A = \frac{\partial f}{\partial \overline{X}}\Big|_{X=X_o}, \quad B = \frac{\partial f}{\partial d}\Big|_{X=X_o}, \quad E = \frac{\partial f}{\partial \overline{v}}\Big|_{X=X_o}$$

#### EXPERIMENTAL VALIDATION OF THE SMALL SIGNAL MODEL



### EQUIVALENT MODEL OF THE RECTIFIER IN THE FREQUENCY DOMAIN





































### STATIC MODEL



# ELABORATION OF THE STATIC MODEL

$$D_{d}' = \frac{2(V_{d} - r_{L}I_{d})}{V_{dc}^{*}}$$

$$D'_{q} = -\frac{2L\omega_{o}I_{d}}{V_{dc}^{*}}$$

$$D_{o}' = \frac{\left(I_{dc}^{+} - I_{dc}^{-}\right)}{\alpha I_{d}}$$

# FUNCTION DUTY CYCLE- DC VOLTAGE





### FUNCTION DUTY CYCLE-DIRECT CURRENT





### FUNCTION DUTY CYCLE-DC VOLTAGES UNBALANCE










#### **QUASI-LINEAR CONTROL**

- This concept has been recently introduced by Kelemen/Bensoussan in 2002, for <u>SISO</u> <u>continuous systems</u>.
- A quasi-linear controller is a lead/lag compensator, which gain may be indefinely increased and its poles are accordingly adjusted:

$$G_{c}(\mathbf{s}) = \frac{k \prod_{i=1}^{r-1} (\mathbf{s} + z_{i})}{\prod_{i=1}^{r-1} (\mathbf{s} + \underline{a_{i}k^{f}})}$$

# Arbitrarily fast and robust tracking by feedback





$$G_c(s) = \frac{k(s+1)}{(s+2)}$$

### Step responses of a conventional lead/lag controller for different gain values



*k* = **1** 





#### Linear controller: paranmetric variations and system stability



### Step responses of a quasi-linear controller for different gain values

$$G_{c}(s) = \frac{k(s+z_{1})(s+z_{2})}{(s+a_{1}k^{f})(s+a_{2}k^{f})}, \quad f = 5/12$$



## Quasi-linear controller: parametric variations and system stability



#### Design of quasi-linear controllers for Vienna converter





#### QUASI-LINEAR CONTROLLERS PERFORMANCES IN CLOSED-LOOP

Variable	Paramètres dans	Paramètres	Pôles dans le continu	t <sub>s</sub> (ms)
contrôlée	le continu	dans le discret		
	$z_{id} = 1$	$z_{idd} = 1$	$-6.315 \times 10^{7}$	
i <sub>d</sub>	$a_{id} = 0.05$	$a_{idd} = -0.9524$	$-9.69 \times 10^{4}$	0.31
	$f_{id} = 0.75$	$k_{idd} = -10.0265$	-2045	
	$k_{id} = -(10^{12})$		-52.07	
	$a_{iq} = \sqrt{2}$	$a_{iqd} = -0.8880$	$-1.4137 \times 10^{5}$	
$i_q$	$f_{iq} = 0.5$	$k_{iqd} = -9.3471$	-10 + 420 i	0.46
	$k_{iq} = -(10^{10})$		-10 – 420 i	
			-20	
	$a_{\Delta v} = 0.25$	$z_{\Delta v d} = 0.9997$	-8910	
$\Delta v_{dc}$	$z_{\Delta v} = 2$	$a_{\Delta vd} = -0.0586$	-542.2	50.6
	$f_{\Delta v} = 0.75$	$k_{\Delta vd} = 4.4104$	-2	
	$k_{\Delta\nu} = (10^8)$			
	$a_v = 0.25$	$z_{vd} = 1$	-1160	
Vdc	$z_v = 2$	$a_{vd} = 0.2873$	-265.2	712
	$f_v = 0.75$	$k_{vd} = 0.0133$	-6.39	
	$k_{\nu} = (10^6)$			

#### REAL-TIME IMPLEMENTATION OF THE QUASI-LINEAR CONTROL SCHEME





#### CONTROL TECHNIQUES APPLIED TO THE VIENNA RECTIFIER



#### NONLINEARITY-COMPENSATING CONTROL SCHEME



#### APPLICATION TO THE VIENNA CONVERTER



$$\begin{bmatrix} i_d(k+1)\\ i_q(k+1)\\ \Delta v_{dc}(k+1) \end{bmatrix} = F_d(X, \theta_d) + G_d(X, \theta_d)d(k)$$
$$= I_d(k) = I_d^{-1}(X, \theta_d, v) = G_d^{-1}(X, \theta_d)[-F_d(X, \theta_d) + v(k)]$$

#### STABILIZING CONTROL LAWS

$$i_{d}(k+1) = a_{i}i_{d}(k-1) + b_{i}i_{d}(k) + c_{i}i_{d}^{*}(k)$$

$$i_{q}(k+1) = a_{i}i_{q}(k-1) + b_{i}i_{q}(k) + c_{i}i_{q}^{*}(k)$$

$$\Delta v_{dc}(k+1) = a_{\Delta v}\Delta v_{dc}(k-2) + b_{\Delta v}\Delta v_{dc}(k-1) + c_{\Delta v}\Delta v_{dc}(k) + d_{\Delta v}\Delta v_{dc}^{*}(k-1)$$

$$+ e_{\Delta v}\Delta v_{dc}^{*}(k)$$

$$v_{1}(k) = a_{i}i_{d}(k-1) + b_{i}i_{d}(k) + c_{i}i_{d}^{*}(k)$$

$$v_{2}(k) = a_{i}i_{q}(k-1) + b_{i}i_{q}(k) + c_{i}i_{q}^{*}(k)$$

$$v_{3}(k) = a_{\Delta v}\Delta v_{dc}(k-2) + b_{\Delta v}\Delta v_{dc}(k-1) + c_{\Delta v}\Delta v_{dc}(k) + d_{\Delta v}\Delta v_{dc}^{*}(k-1) + e_{\Delta v}\Delta v_{dc}^{*}(k)$$

#### REFERENCE CURRENTS GENERATION

$$v_{dc}(k+1) = v_{dc}(k) + \frac{T_s}{C_{dc}} \left[ \frac{3}{v_{dc}(k)} \hat{V} \hat{I}^*(k) - i_{dc}^+(k) - i_{dc}^-(k) \right]$$
$$\hat{V} = \sqrt{v_d^2 + v_q^2}$$

$$\hat{I^{*}}(k) = \frac{C_{dc}v_{dc}(k)v_{4}(k) + v_{dc}(k)(i_{dc}^{+}(k) + i_{dc}^{-}(k))}{3\sqrt{v_{d}^{2}(k) + v_{q}^{2}(k)}}$$

$$\hat{I^{*}}(k) = \frac{C_{dc}v_{dc}(k)v_{4}(k) + v_{dc}(k)(i_{dc}^{+}(k) + i_{dc}^{-}(k))}{\sqrt{v_{d}^{2} + v_{q}^{2}}}v_{d}$$

$$\hat{I^{*}}_{q} = \frac{\hat{I^{*}}}{\sqrt{v_{d}^{2} + v_{q}^{2}}}v_{q}$$

#### DC VOLTAGE REGULATION

$$v_{dc}(k+1) = v_{dc}(k) + T_s v_4(k) \Leftrightarrow \frac{v_{dc}(k)}{v_4(k)} = \frac{T_s}{(z-1)}$$



$$H_{v}(z) = \frac{K_{v}(z + a_{v})}{(z - 1)(z + z_{v})}$$



#### STEADY STATE RESULTS



#### CONTROL TECHNIQUES APLLIED TO THE VIENNA CONVERTER



#### MODEL REFERENCE ADAPTIVE CONTROL





#### ADAPTIVE LINEARIZING CONTROL LAWS



#### STABILIZING ADAPTIVE CONTROL LAWS

$$\hat{v}_i = -\sum_{j=1}^{r_i} k_j^i y_j^{i} + R_i$$



#### PARAMETERS ADAPTATION SCHEME

$$\overset{\bullet}{\theta} = -\Delta \overset{\bullet}{\theta} = \Gamma \sum_{i=1}^{m} W_i^{1T} \left( \begin{array}{c} & & & \\ X, & \theta, u \end{array} \right) P_i \overset{\bullet}{\eta}_i^i$$

#### APPLICATION TO THE VIENNA RECTIFIER



#### DERIVATION OF THE LINEARIZING INPUTS

$$\dot{y}_1 = i_d$$
,  $\dot{y}_2 = i_q$ ,  $\dot{y}_3 = \Delta v_{dc}$ 

$$\begin{array}{l} \stackrel{(I)}{y_{1}} = \left( \stackrel{}{y_{1}(k)} + \stackrel{}{\theta_{1d}} + \stackrel{}{\theta_{2d}} \stackrel{}{y_{2}(k)} - \stackrel{}{\theta_{3d}} \stackrel{}{X_{4}(k)} \stackrel{}{u_{1}(k)} \right) + \left( \varDelta \theta_{1d} + \varDelta \theta_{2d} \stackrel{}{y_{2}(k)} - \varDelta \theta_{3d} \stackrel{}{X_{4}(k)} \stackrel{}{u_{1}(k)} \right); \\ \stackrel{(I)}{y_{2}} = \left( \stackrel{}{y_{2}(k)} + \stackrel{}{\theta_{4d}} - \stackrel{}{\theta_{2d}} \stackrel{}{y_{1}(k)} - \stackrel{}{\theta_{3d}} \stackrel{}{X_{4}(k)} \stackrel{}{u_{2}(k)} \right) + \left( \varDelta \theta_{4d} - \varDelta \theta_{2d} \stackrel{}{y_{2}(k)} - \varDelta \theta_{3d} \stackrel{}{X_{4}(k)} \stackrel{}{u_{2}(k)} \right); \\ \stackrel{(I)}{y_{3}} = \left[ \stackrel{}{y_{3}(k)} + \stackrel{}{\theta_{6d}} \stackrel{}{y_{1}(k)} \stackrel{}{u_{1}(k)} + \stackrel{}{\theta_{7d}} \stackrel{}{y_{3}(k)} + \stackrel{}{\theta_{8d}} \stackrel{}{X_{4}(k)} - \stackrel{}{\theta_{5d}} \stackrel{}{\frac{}{X_{4}(k)}} \left( \stackrel{}{y_{1}(k)} \stackrel{}{u_{1}(k)} + \stackrel{}{y_{2}(k)} \stackrel{}{u_{2}(k)} \right) \right] \\ + \left[ \varDelta \theta_{6d} \stackrel{}{y_{1}(k)} \stackrel{}{u_{1}(k)} + \varDelta \theta_{7d} \stackrel{}{y_{3}(k)} + \varDelta \theta_{8d} \stackrel{}{X_{4}(k)} - \varDelta \theta_{5d} \stackrel{}{\frac{}{X_{4}(k)}} \left( \stackrel{}{y_{1}(k)} \stackrel{}{u_{1}(k)} + \stackrel{}{y_{2}(k)} \stackrel{}{u_{2}(k)} \right) \right] \end{array} \right]$$

#### DERIVATION OF THE STABILIZING CONTROL LAWS

$$\hat{v}_i(k) = -k_1^i \hat{y}_i(k) + b_i y_{ref}^i(k); \quad i = \{1, 2, 3\}$$



$$\begin{bmatrix} k_1^1 \\ k_1^2 \\ k_1^3 \\ k_1^3 \end{bmatrix} = \begin{bmatrix} .001 \\ .001 \\ .1 \end{bmatrix}, \begin{bmatrix} b_1 \\ b_2 \\ b_3 \end{bmatrix} = \begin{bmatrix} .999 \\ .999 \\ .9 \end{bmatrix}$$



#### ZERO DYNAMICS STABILITY

>Internal dynamics describing the DC voltage  $(v_{dc})$  ont été ignorées lors de la phase de linérisation.

>The zero dynamics describe the internal behaviour of the system when the inputs and initial conditions are chosen in a way to zero the outputs:

$$v_{dc}(k+1) = v_{dc}(k) \left[ 1 - \frac{T_s}{2C_{dc}} \left( \frac{1}{R_{dc}^+} + \frac{1}{R_{dc}^-} \right) \right]$$

is asymptotically stable for:

$$\left[1 - \frac{T_s}{2C_{dc}} \left(\frac{1}{R_{dc}^+} + \frac{1}{R_{dc}^-}\right)\right] \neq 0$$

#### STEADY STATE RESULTS







Critère	Conditions	Entités comparées	QL	NL	NLA
	Toutes	Pas de calcul (μs)	38	39	39
Efforts		Nombre de variables mesurées	5	10	10
d'implantation		Nombre de paramètres à régler	11	11	22
		<b>THD</b> (%)	6	6	7
	Puissance nominale	FP	0.99	0.99	0.98
		FDP	1	1	1
Performances en	50% de la	<b>THD</b> (%)	7	9	13
régime permanent	puissance	FP	0.98	0.97	0.96
	nominale	FDP	1	1	1
	30% de la	<b>THD</b> (%)	10	15	19
	puissance	FP	0.97	0.95	0.94
	nominale	FDP	1	1	1
	Puissance	<b>THD</b> (%)	6.5	8	10
	nominale	Dépassement en tension (%)	9	0	2.3
Transitoires durant	$R_{dc}^{-} = 60\% R_{dc}^{+}$	temps de stabilisation à $\pm 5\%$ (s)	1.3	0.24	0.04
un déséquilibre de	50% de la puissance	<b>THD</b> (%)	7	9	11
charges	nominale	Dépassement en tension (%)	16	0	2.5
	$\mathbf{R}_{\mathbf{dc}} = 50 \% \mathbf{R}_{\mathbf{dc}}^{+}$	temps de stabilisation à $\pm 5\%$ (s)	4.32	140	0.05
	30% de la puissance	<b>THD</b> (%)	13	15	16
	nominale	Dépassement en tension (%)	40	0	4.8
	$\mathbf{R}_{\mathbf{dc}} = 30 \% \mathbf{R}_{\mathbf{dc}}^{+}$	temps de stabilisation à $\pm 5\%$ (s)	6.24	0.2	0.08
		Dépassement en courant (%)	45	33	33
Transitoires durant	Puissance	Dépassement en tension (%)	32	8	5
la perte d'une	nominale	temps de stabilisation à $\pm 5\%$ (s)	5	0.02	0.032
phase		Ondulation de la tension DC (%)	10.5	9.3	11

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## STEADY STATE PERFORMANCES





### PHASE PLANE TRAJECTORIES



#### TRANSIENTS DURING DC LOADS UNBALANCE (2)





### TRANSIENTS DURING A TEMPORARY LOSS OF PHASE (a)





## SENSORLESS CONTROL USING AN EXTENDED KALMAN FILTER





### APPLICATION TO THE VIENNA CONVERTER









# CONTRIBUTIONS (1)

- Generalized design approach for the Vienna topology, which may be extended to different power levels and switching frequencies.
- 2. Complete and reliable dynamic model for the Vienna converter.
- 3. Identification of the converter in large and small signal regimes in the synchronous reference frame, which differs from the conventional identification procedures.

# CONTRIBUTIONS (2)

- 4. Adaptation of the new quasi-linear control theory to the discrete systems and multi-input-multi-output systems.
- 5. Use of the sensorless control concept, largely used for electric machines, for the power converters.
- 6. Publication of 4 revue papers (IEEE transactions on Industrial Electronics (3), IEE-Electrical Power Applications (1)).

# CONTRIBUTIONS (3)

 Publication of 13 IEEE conference papers, with lecture comity (IECON (5), ELECTRIMACS (2), ISIE (2), EUROCON (1), ICIT(1), MELECON (1), IAS (1)).